

FIG.1

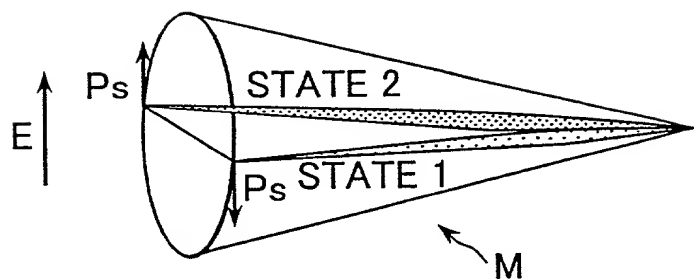


FIG.2

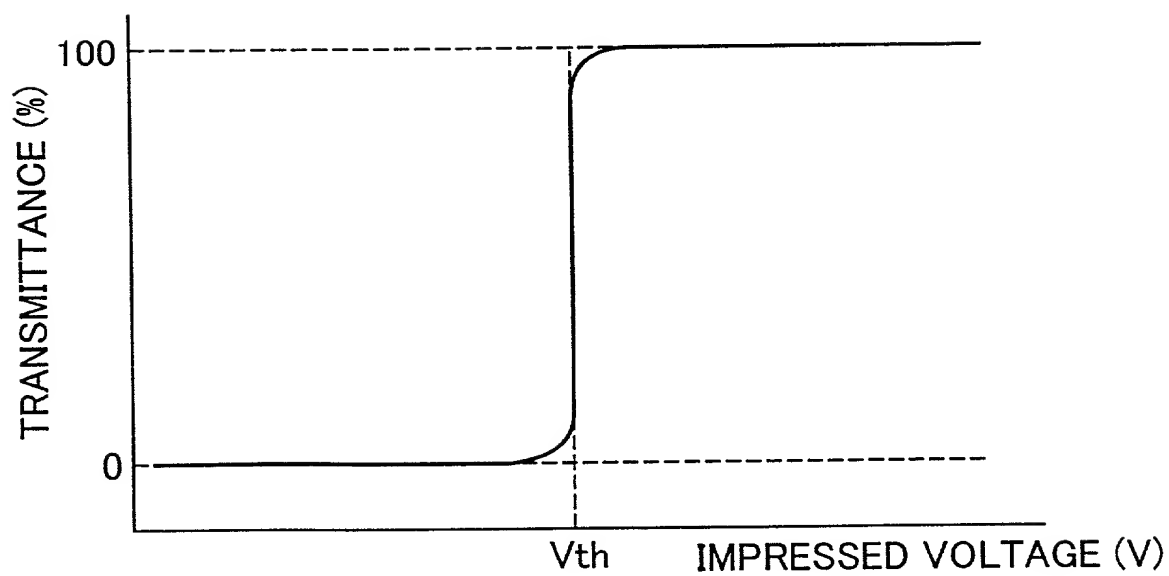


FIG.3

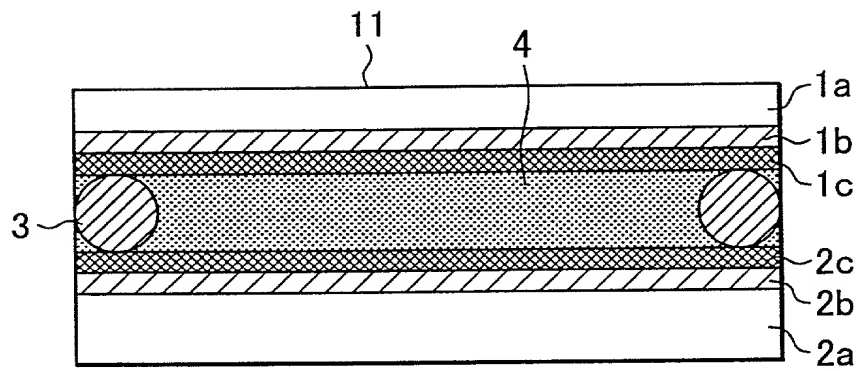


FIG. 4

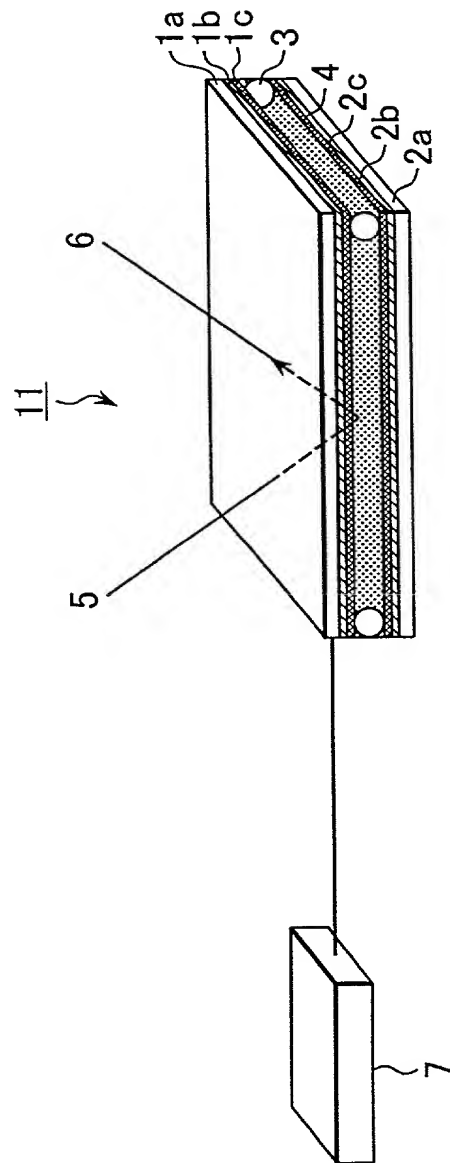


FIG. 5

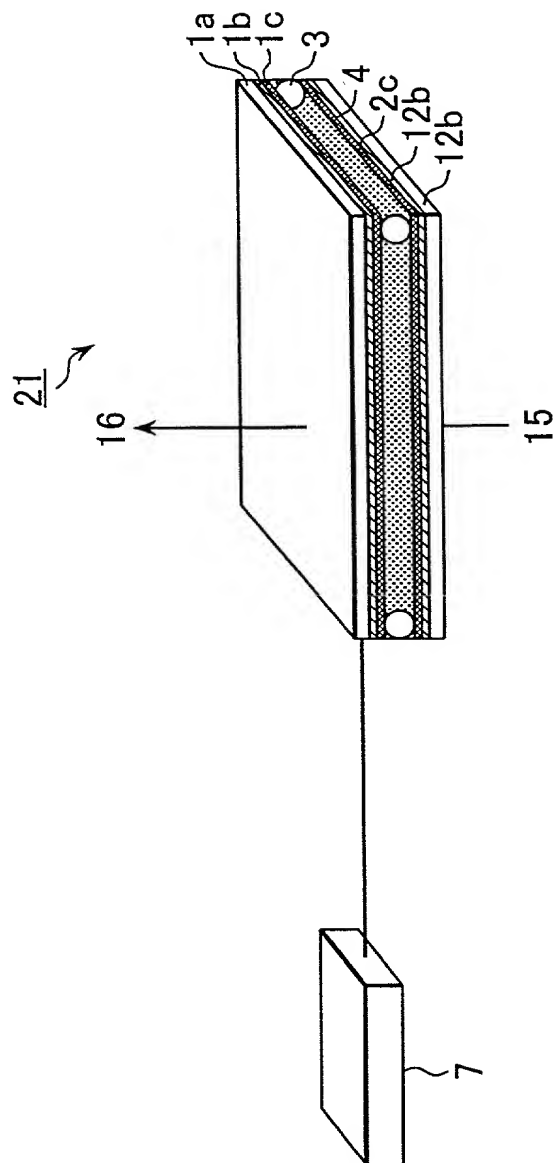


FIG. 6

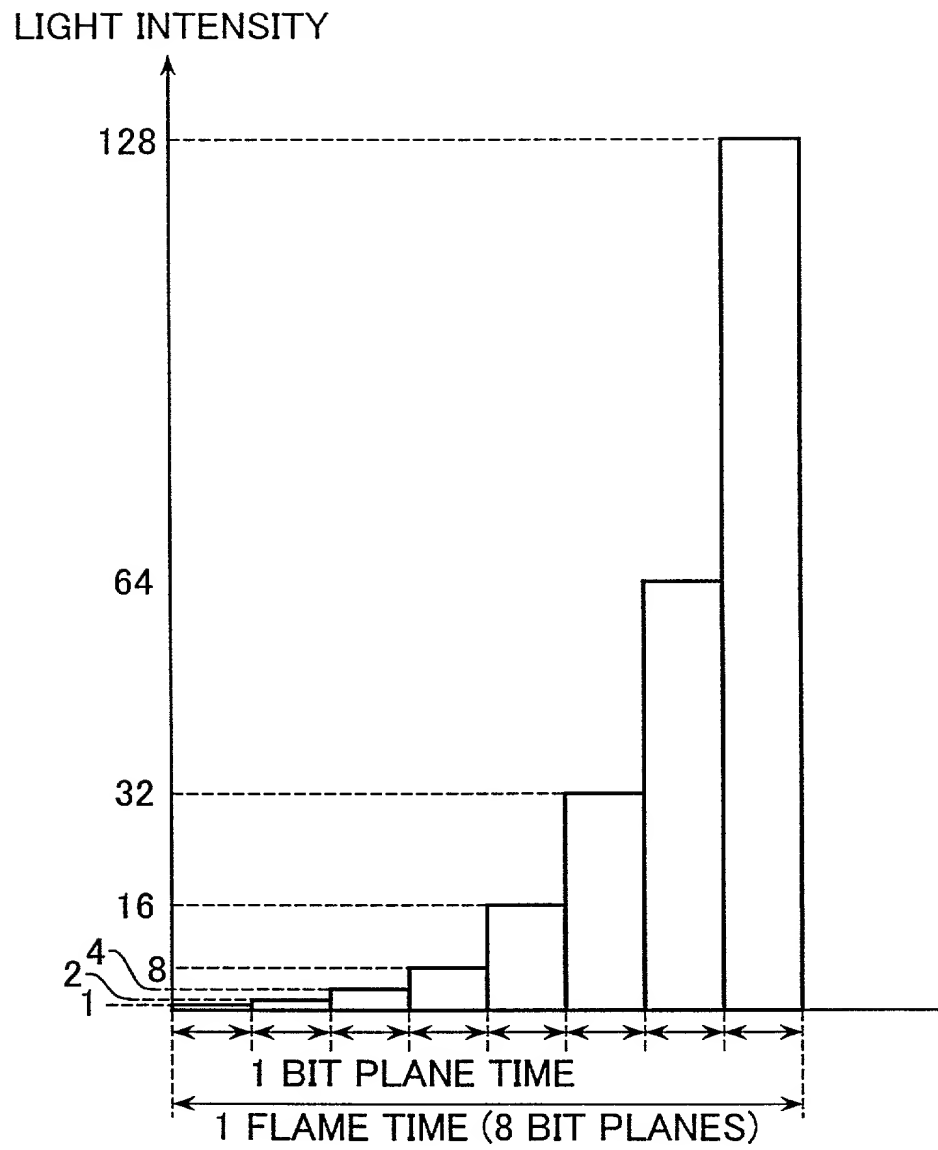
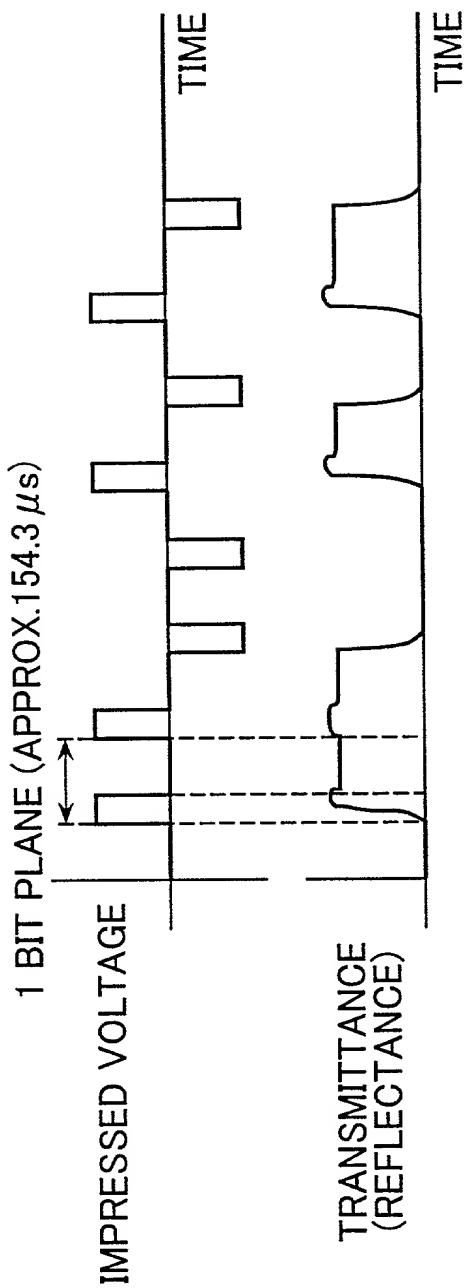


FIG.7



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FIG.8

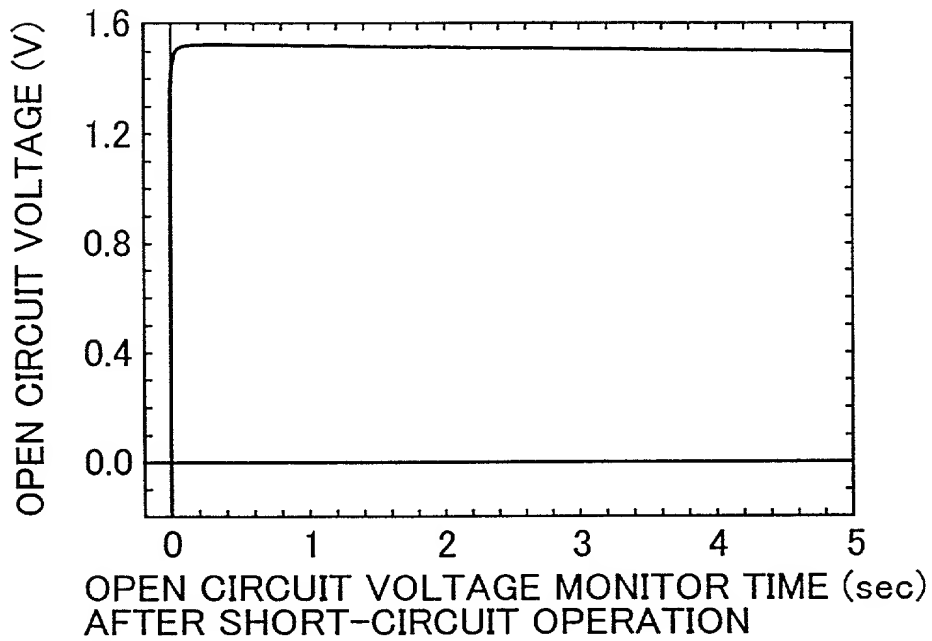
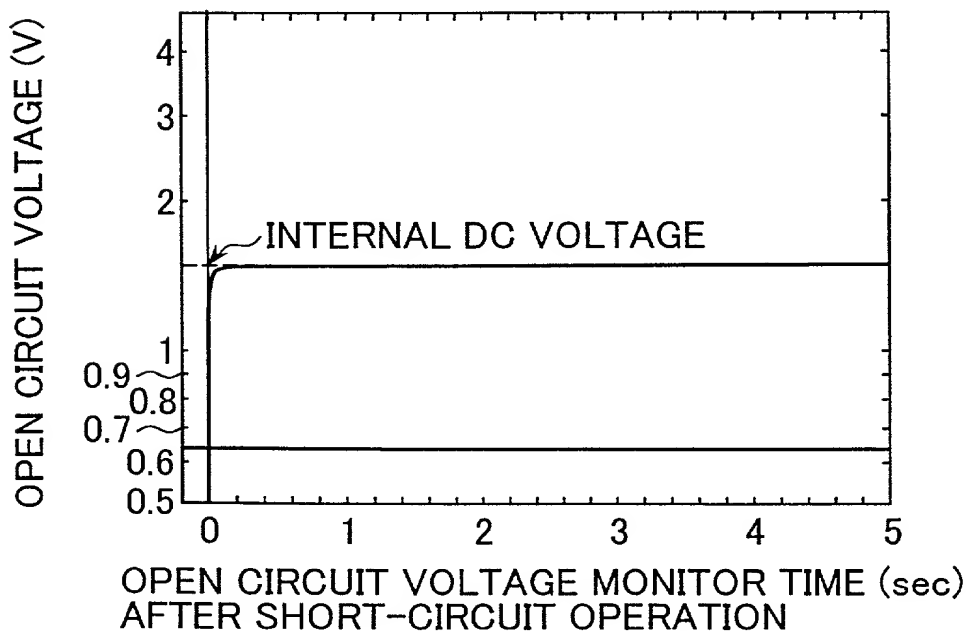


FIG.9



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FIG.10

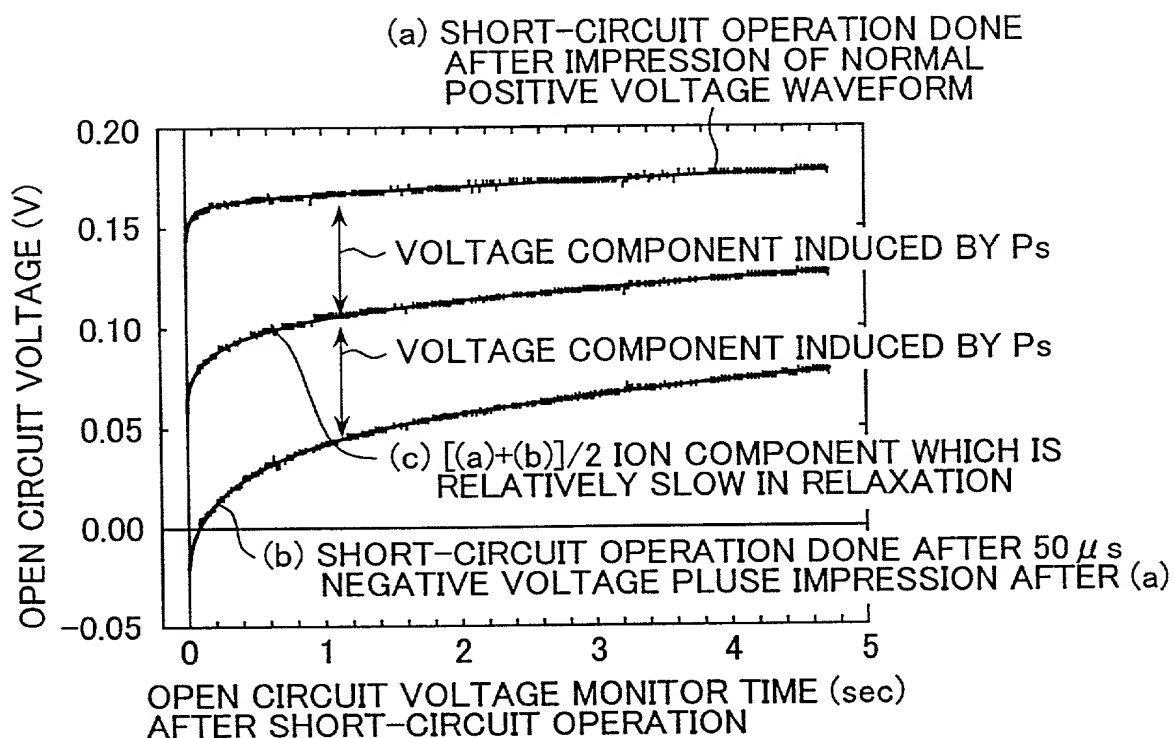
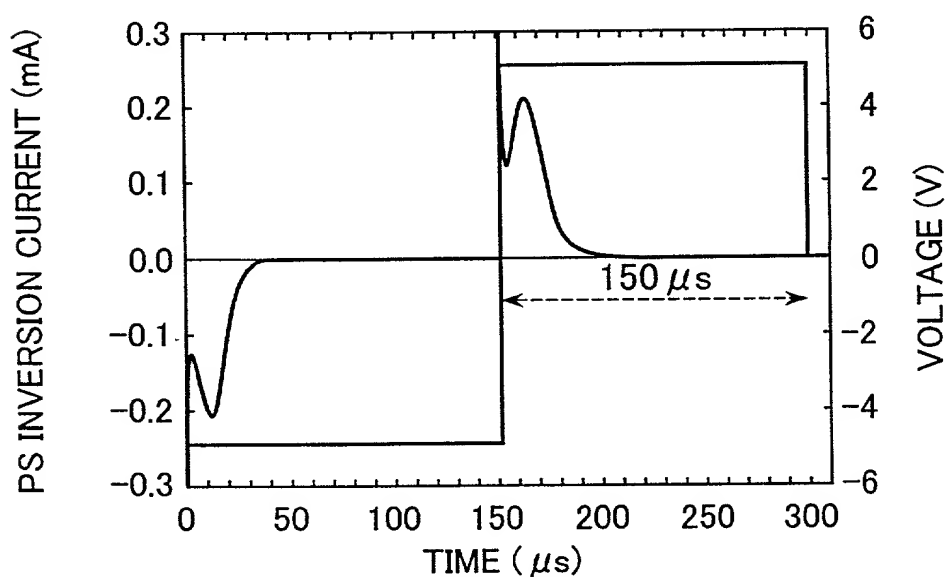


FIG.11



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FIG.12

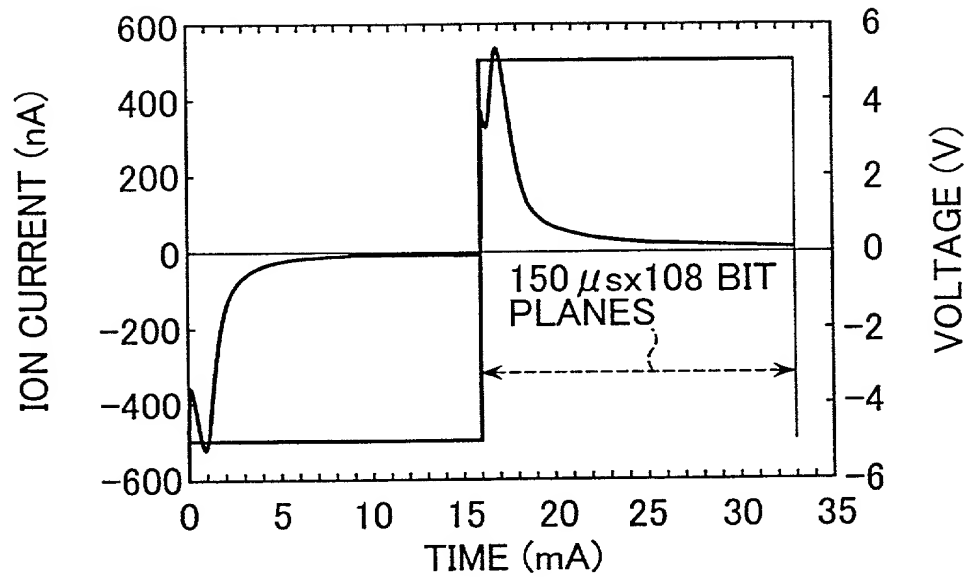


FIG.13

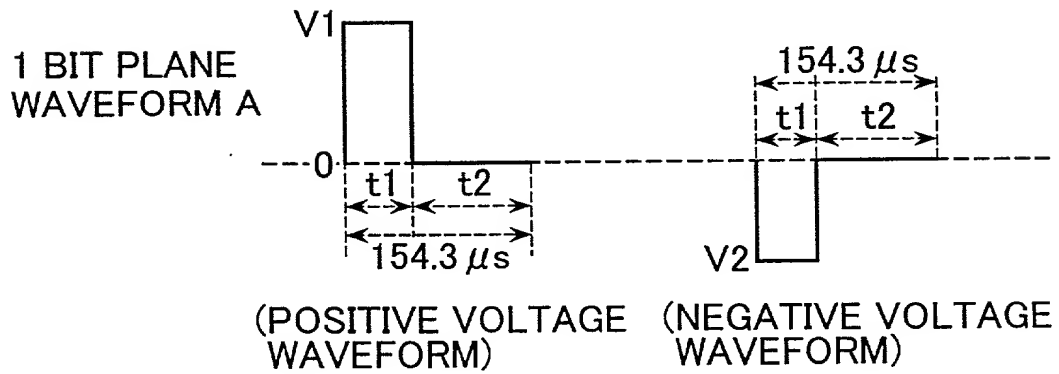


FIG.14

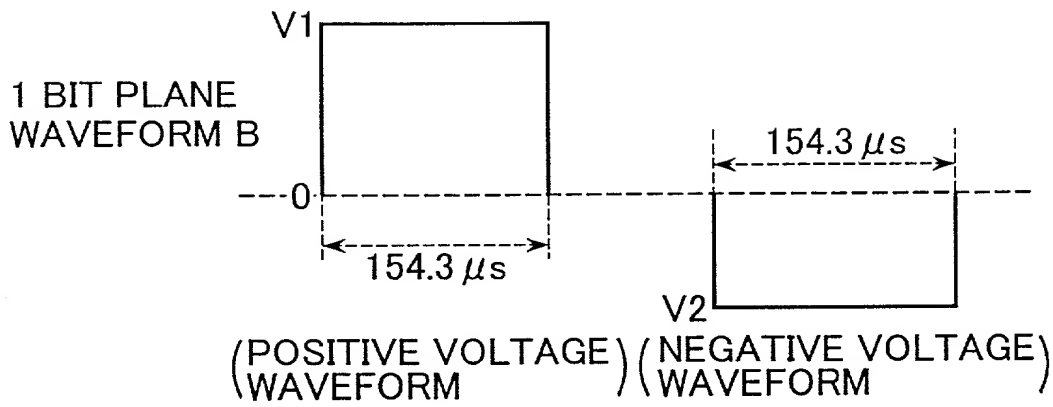


FIG.15

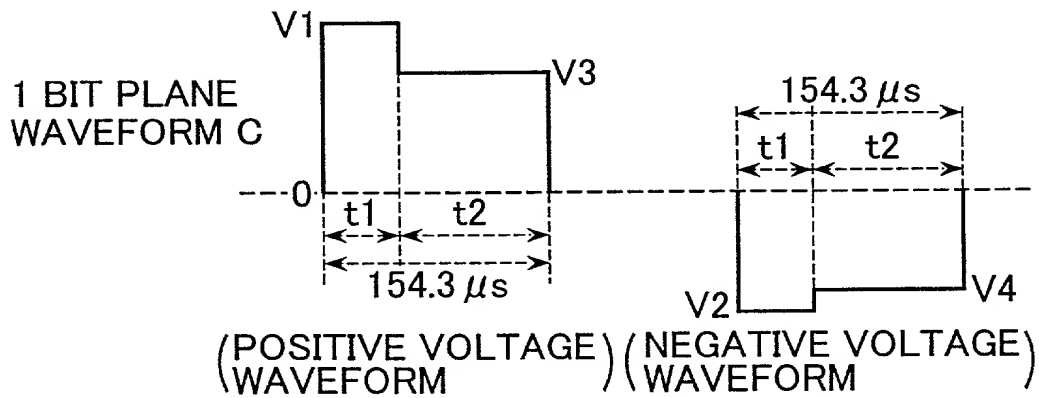


FIG.16

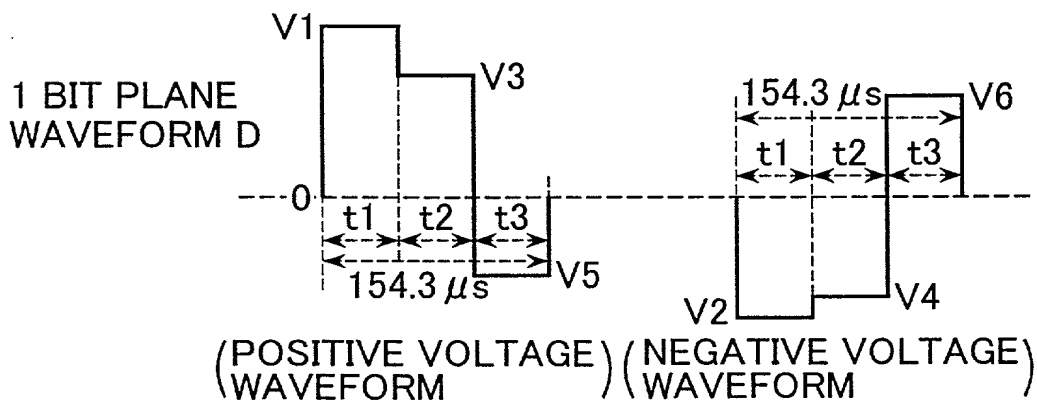


FIG.17

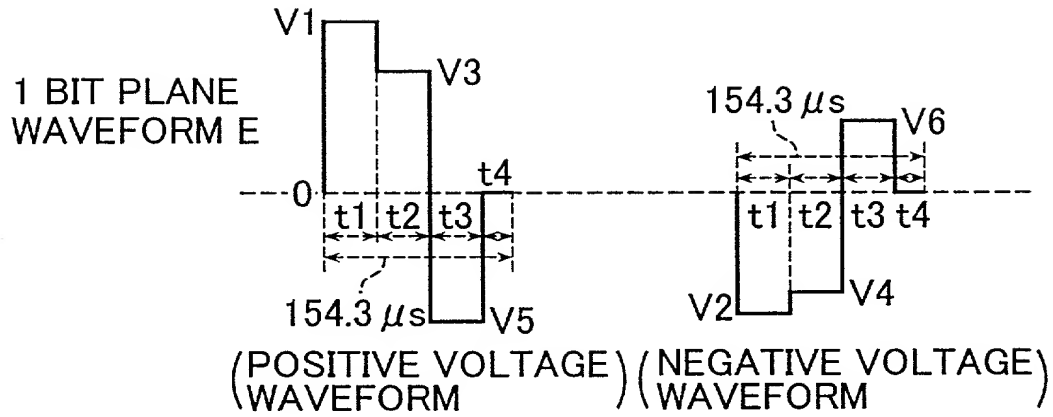


FIG.18

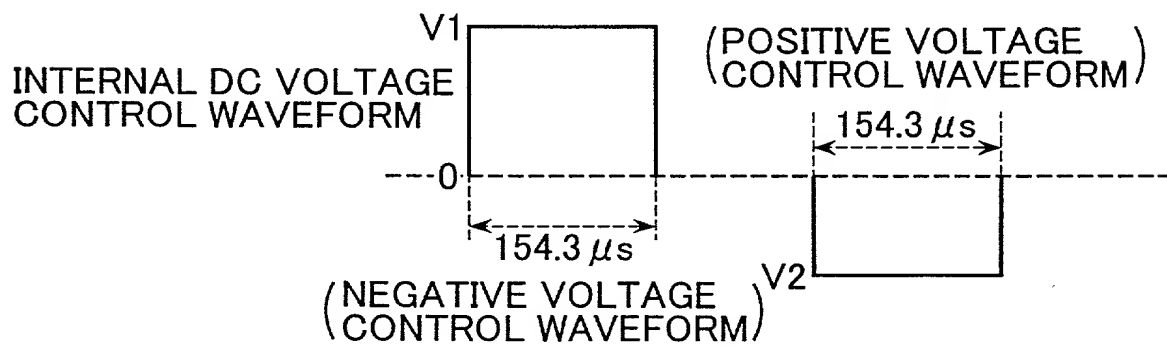
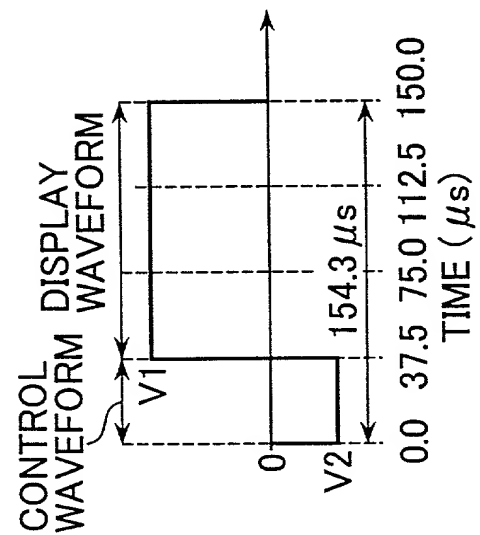


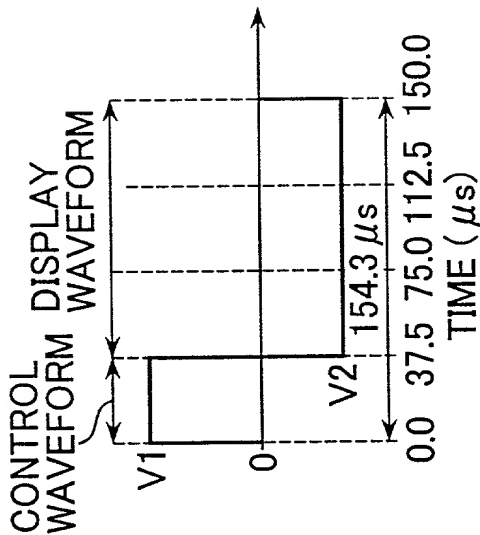
FIG. 19A

FIG. 19B

$V_2=V_4$, $V_1=V_3$, $t_1=t_2=37.5 \mu s$, $t_3=112.5 \mu s$



1 BIT PLANE WAVEFORM FOR DISPLAY
WITH POSITIVE VOLTAGE



1 BIT PLANE WAVEFORM FOR DISPLAY
WITH NEGATIVE VOLTAGE

FIG.20

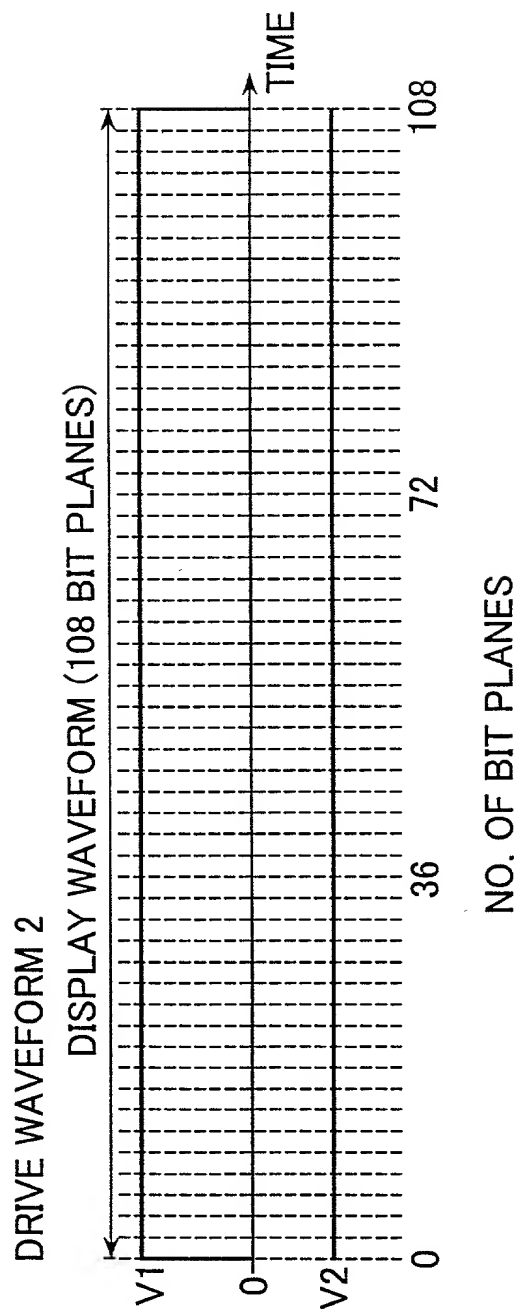


FIG.21

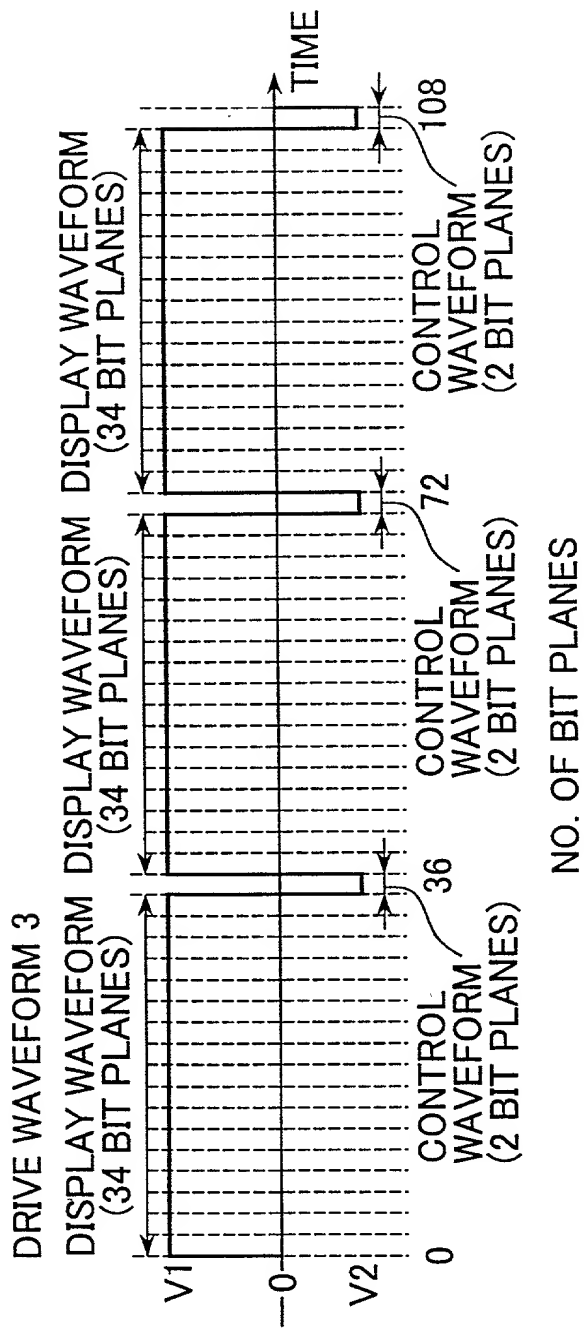
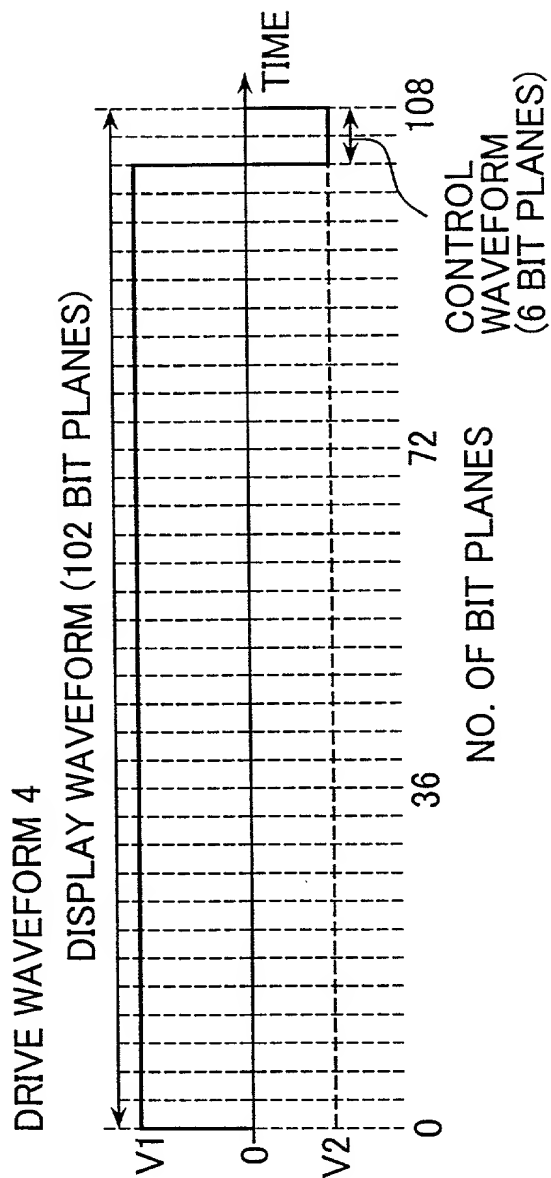
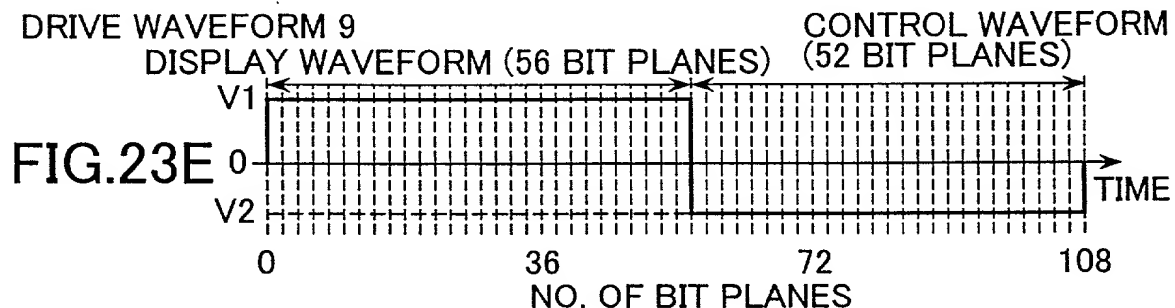
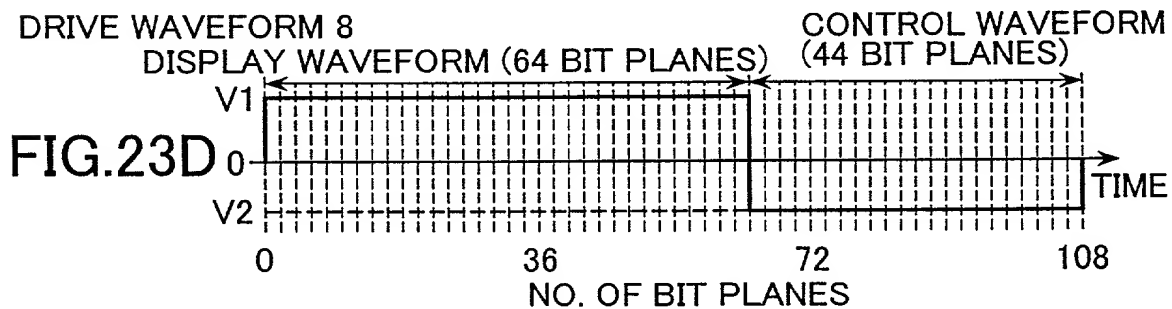
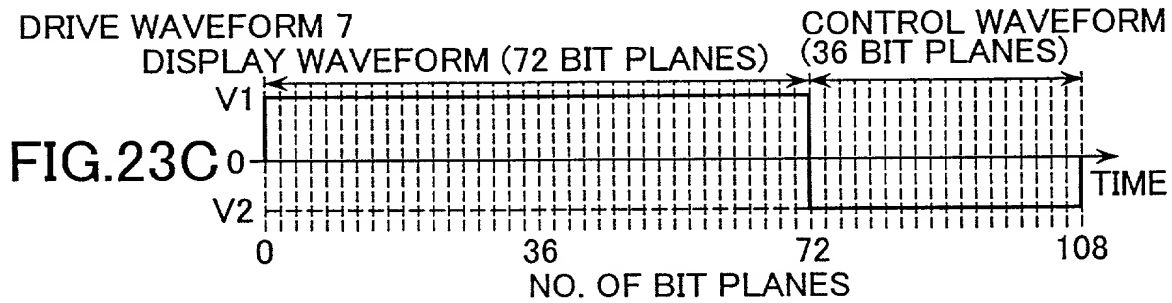
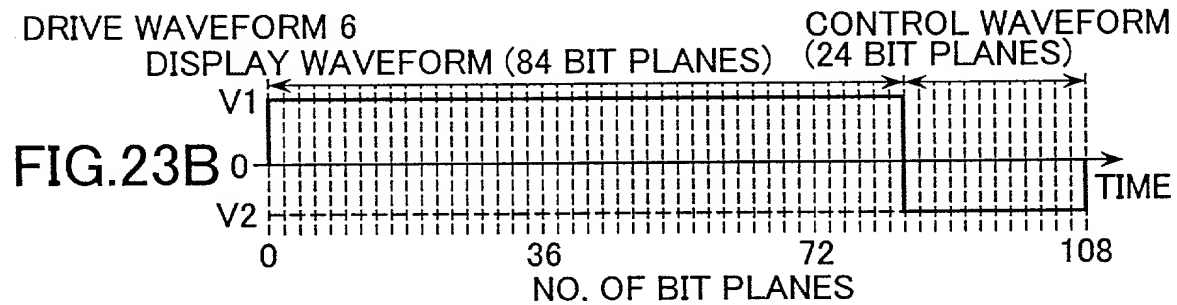
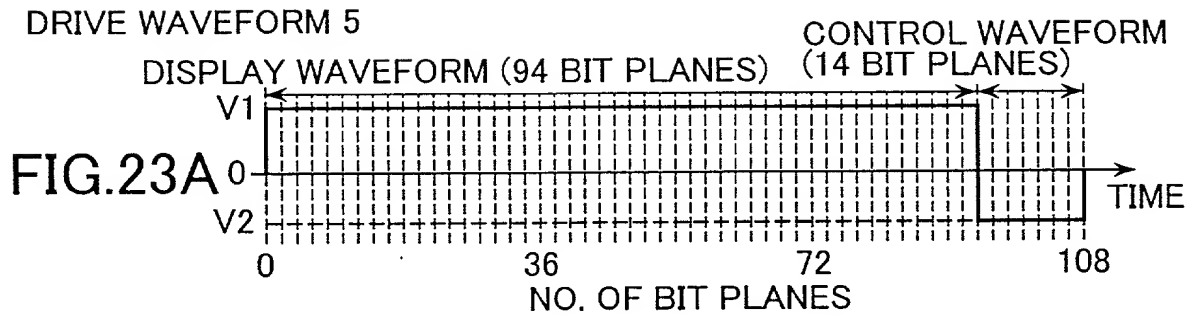
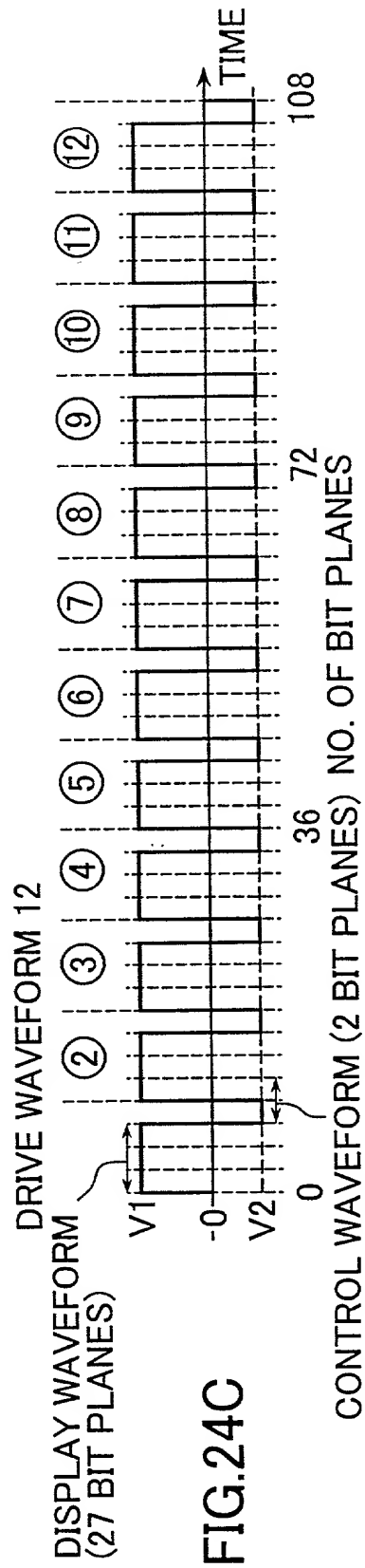
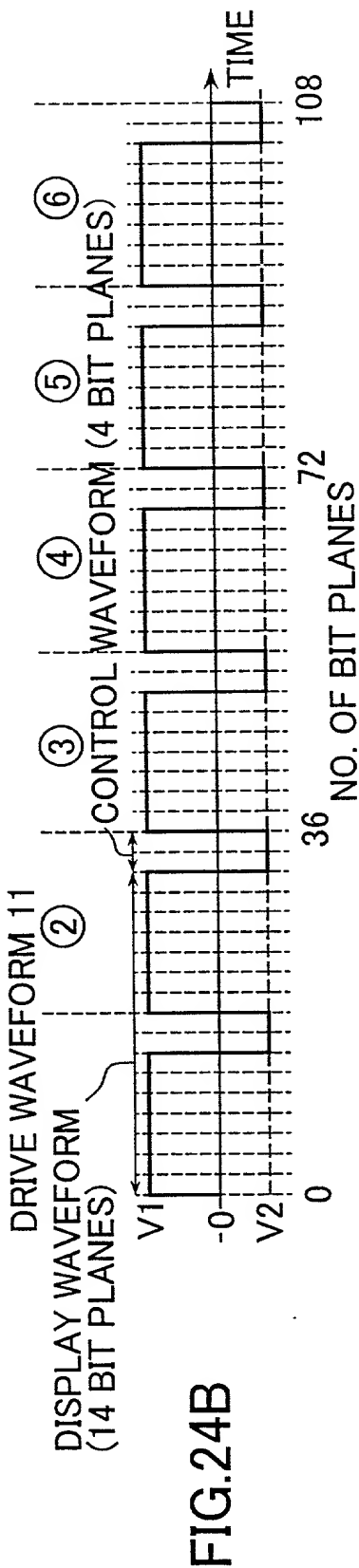
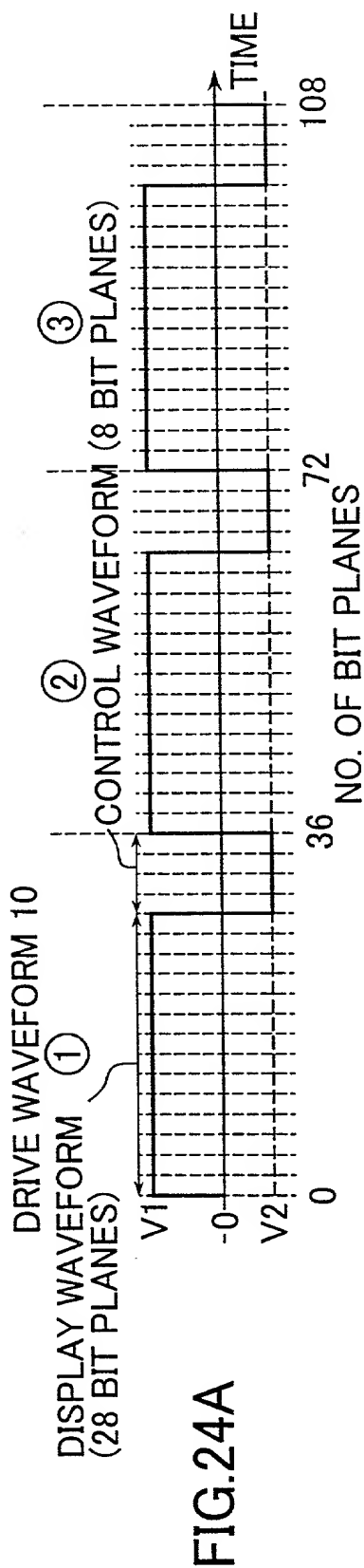


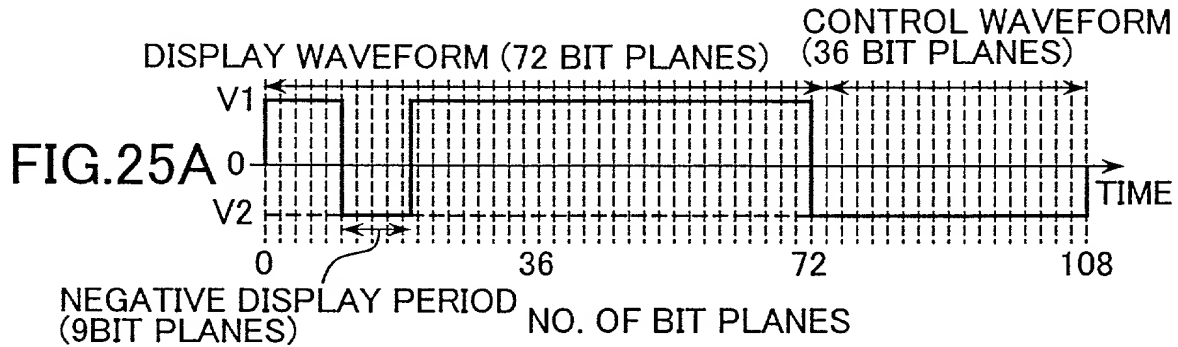
FIG.22



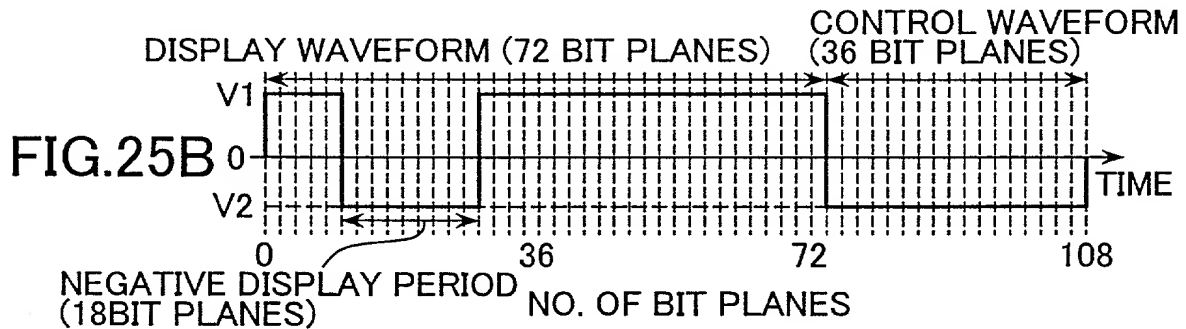




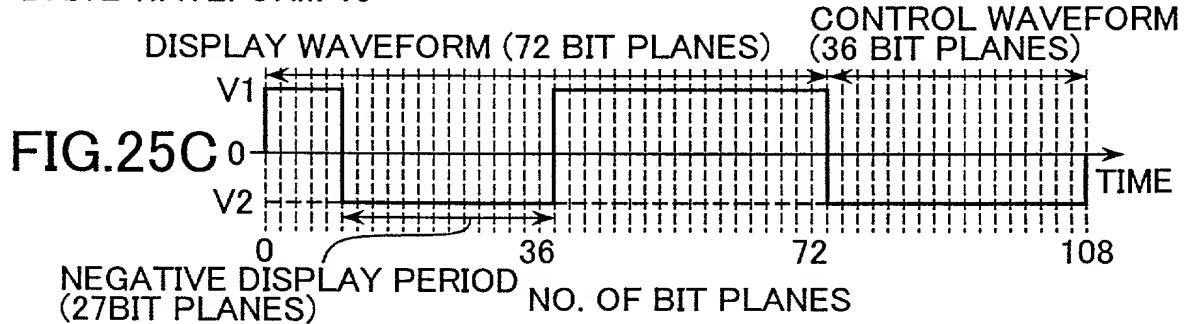
DRIVE WAVEFORM 13



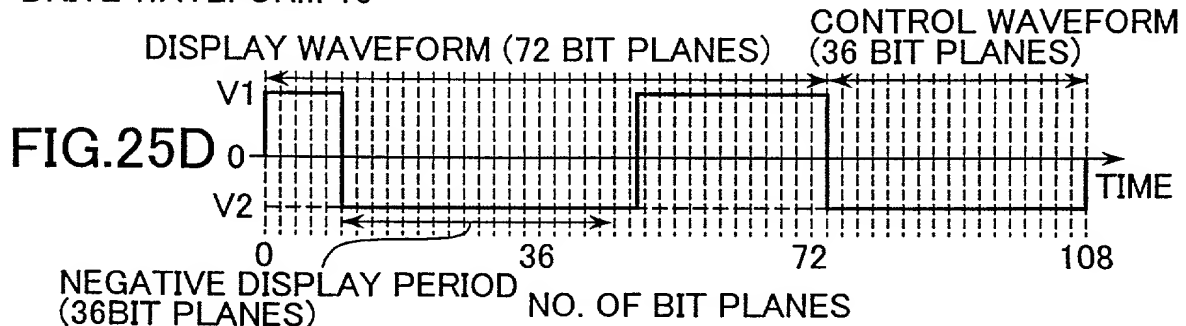
DRIVE WAVEFORM 14



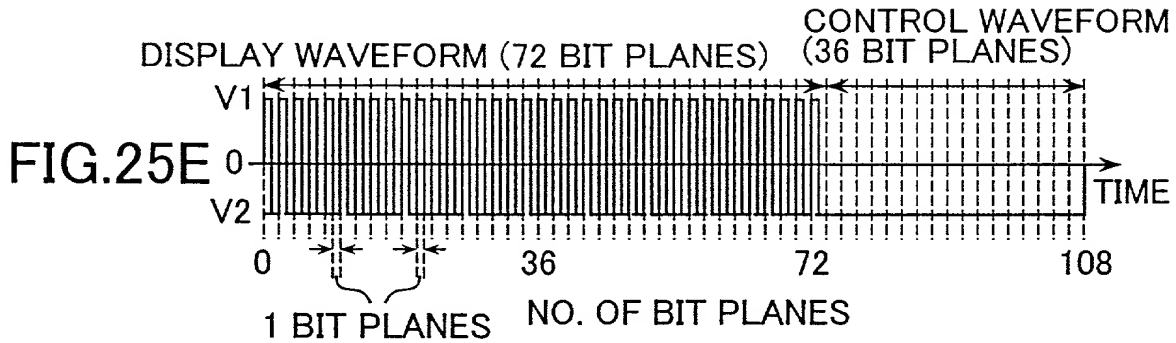
DRIVE WAVEFORM 15



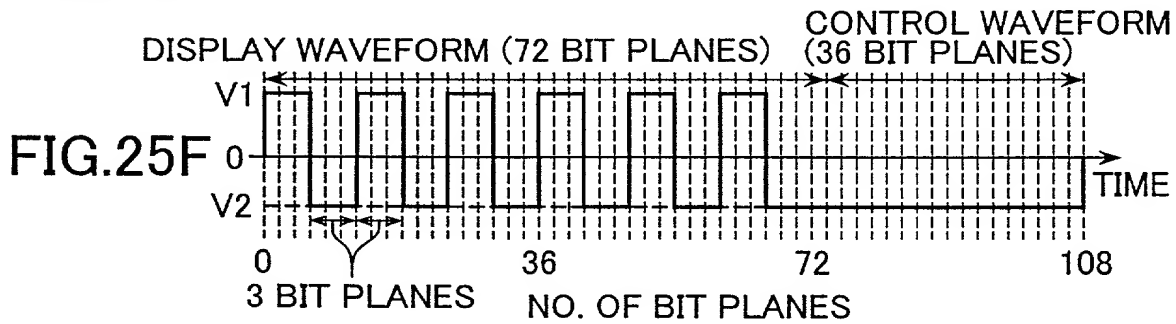
DRIVE WAVEFORM 16



DRIVE WAVEFORM 17



DRIVE WAVEFORM 18



DRIVE WAVEFORM 19

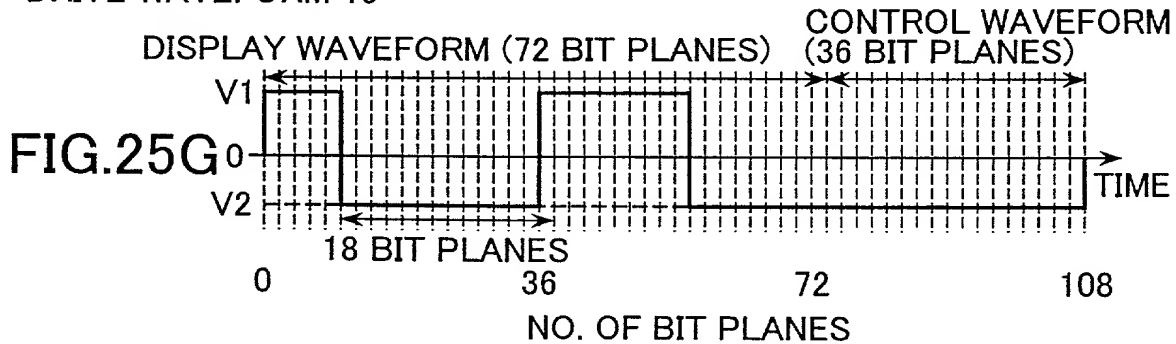


FIG.26

DRIVE WAVEFORM 20

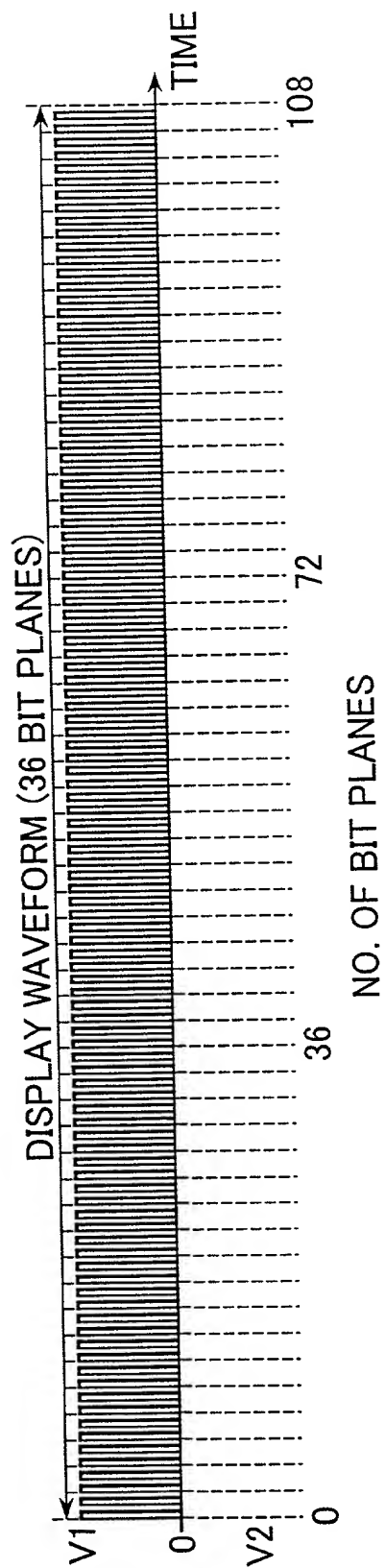


FIG.27

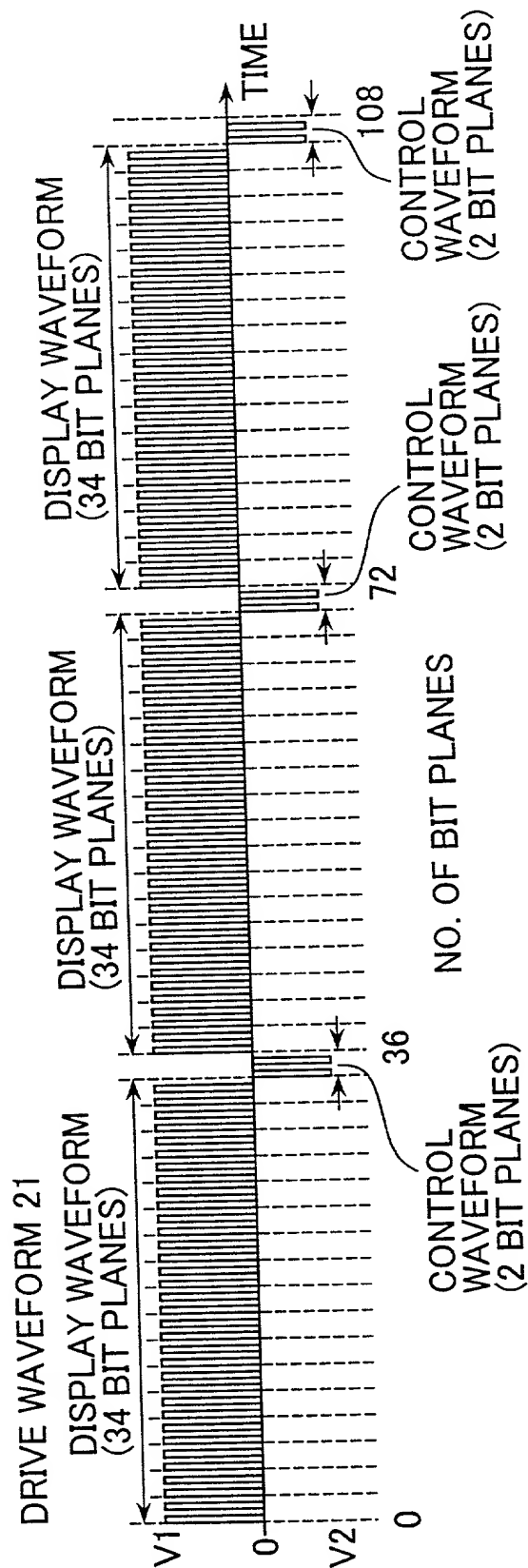
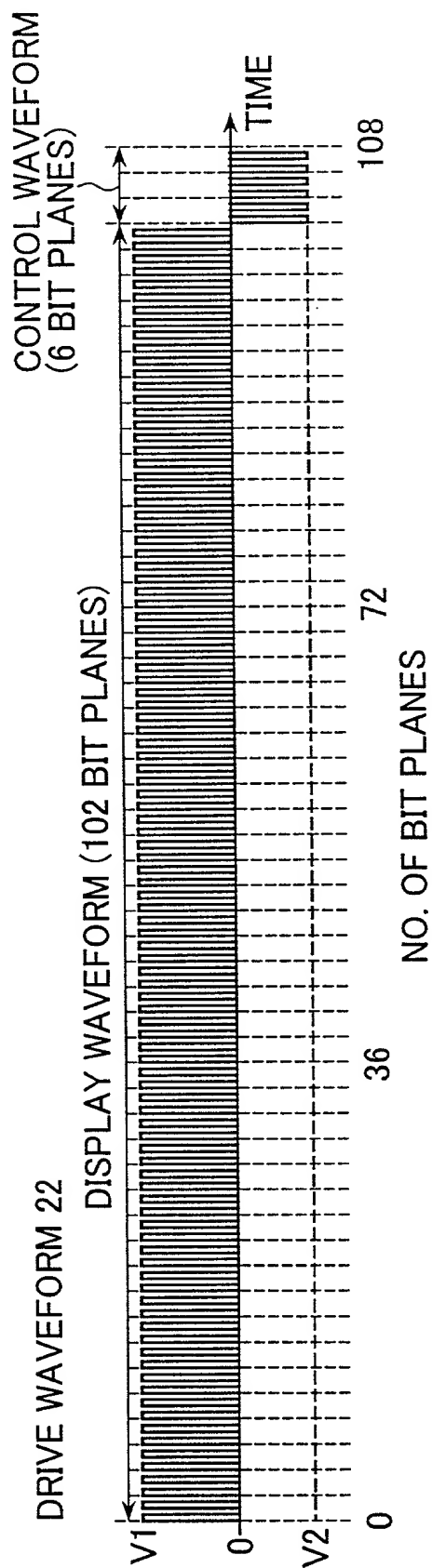
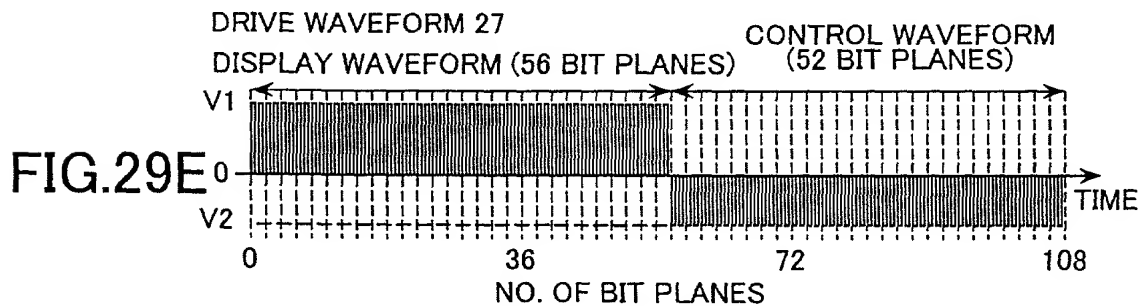
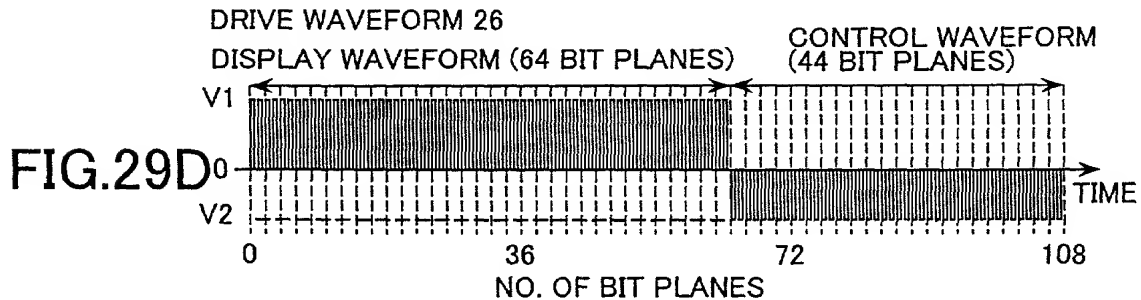
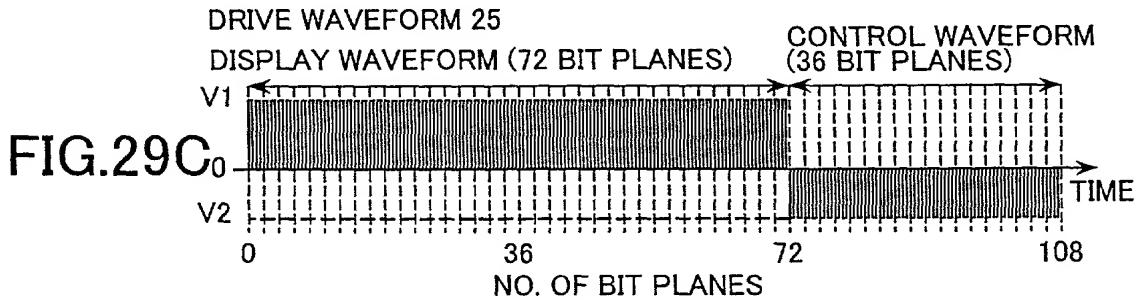
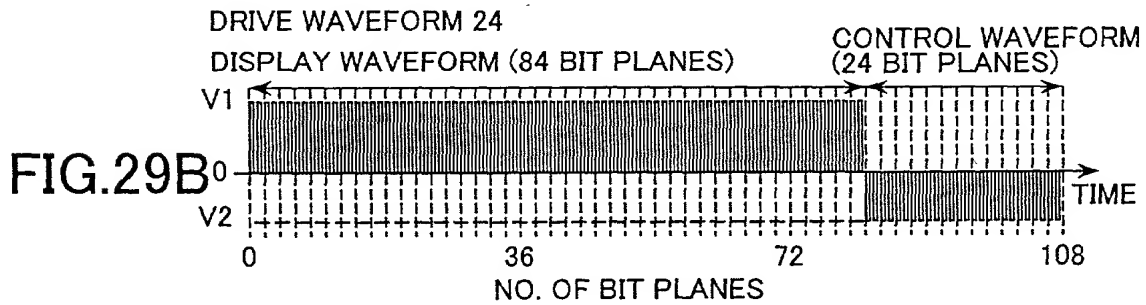
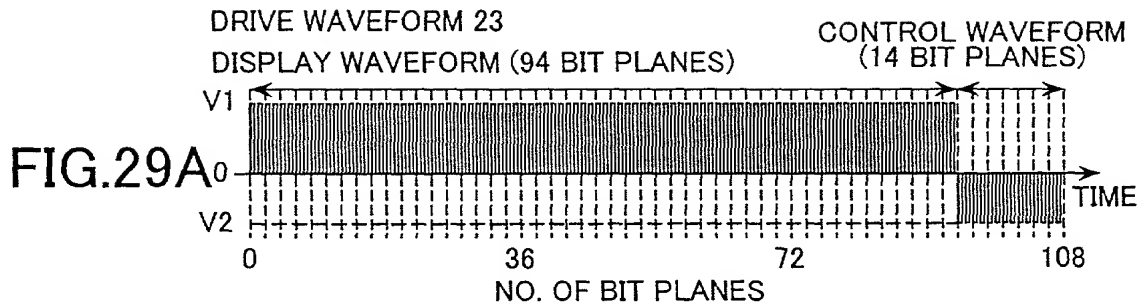


FIG.28





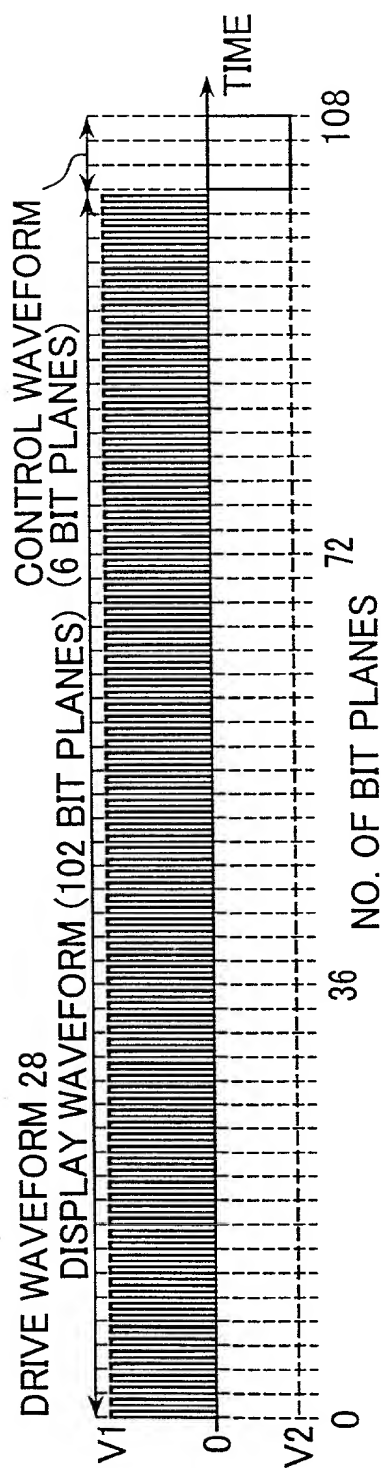


FIG. 30A

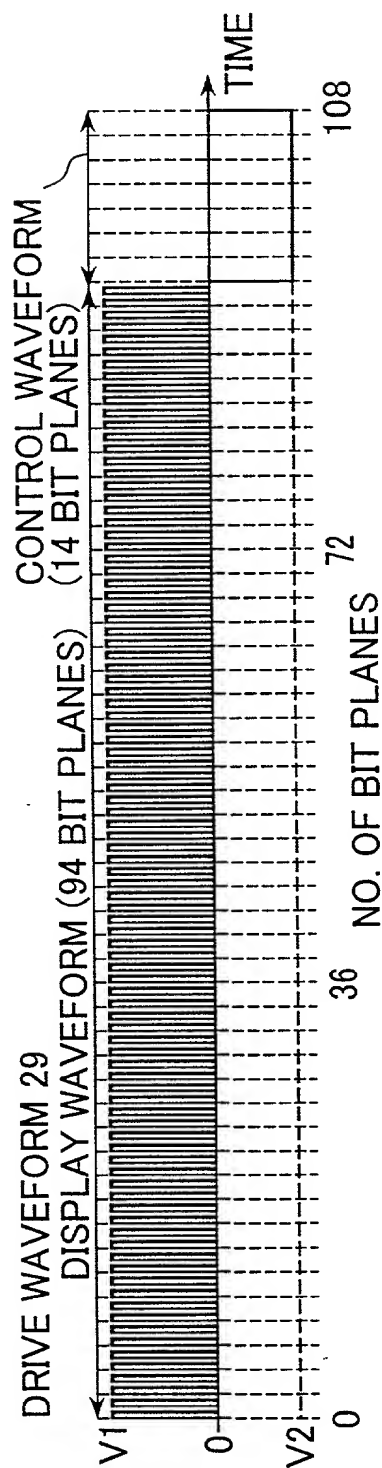


FIG. 30B



FIG. 30C

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FIG.31

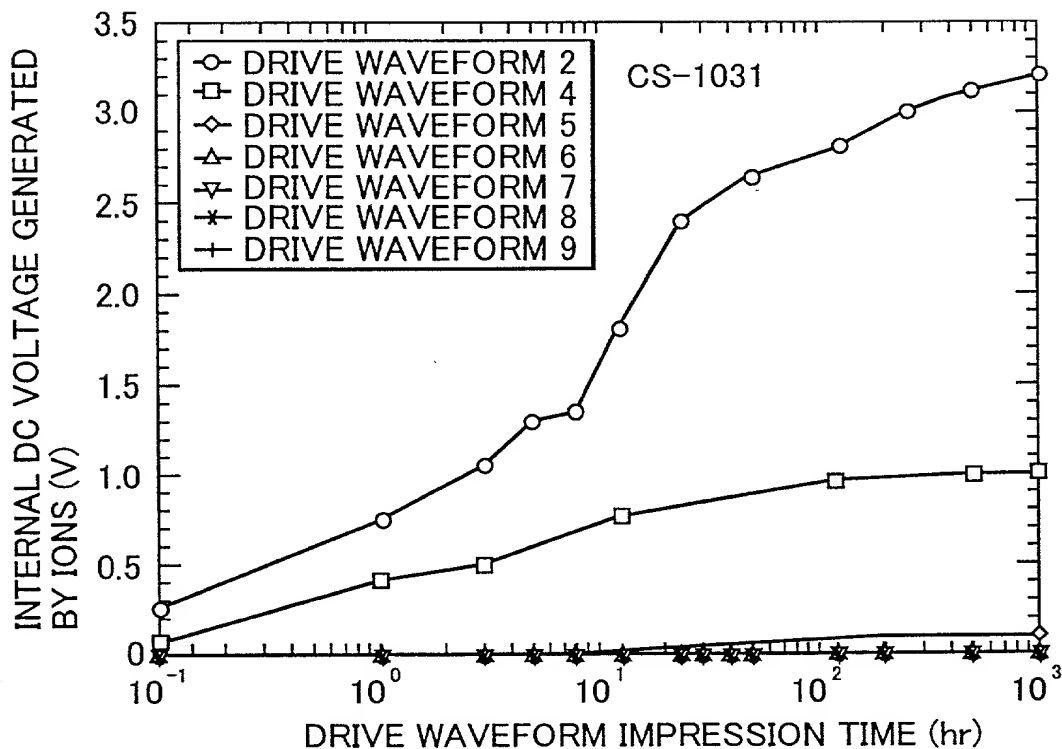


FIG.32

